What is claimed is:

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- 1. A driving apparatus for a plasma display panel, comprising:
- a scan driver for supplying a rising ramp waveform in a set-up interval and a falling ramp waveform in a setdown interval;
 - a temperature sensor for sensing a driving temperature of the panel to generate a bit control signal; and
 - a set-down control signal generator for generating a control signal such that an application time of the falling ramp waveform can be controlled in correspondence with said bit control signal and for applying the control signal to the scan driver.
 - 2. The driving apparatus as claimed in claim 1, wherein said temperature sensor generates different bit control signals at a high temperature and at a temperature less than the high temperature.
 - 3. The driving apparatus as claimed in claim 2, wherein said set-down control signal generator sets a width of said control signal such that a width of the control signal applied at said high temperature is narrower than that of the control signal applied at a temperature less than the high temperature in correspondence with said bit control signal.
- 4. The driving apparatus as claimed in claim 3, wherein said scan driver supplies said falling ramp waveform during a time corresponding to said width of the control signal.

5. The driving apparatus as claimed in claim 2, wherein said temperature sensor divides the high temperature into a plurality of temperature levels, and generates said different bit control signals for each temperature level.

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- 6. The driving apparatus as claimed in claim 5, wherein said set-down control signal generator generates a control signal having a narrower width as the temperature level goes higher, and said scan driver supplies said falling ramp waveform during a time corresponding to said width of the control signal.
- 7. A driving apparatus for a plasma display panel, 15 comprising:

a scan driver for supplying a rising ramp waveform in a set-up interval and a falling ramp waveform in a set-down interval;

a temperature sensor for sensing a driving 20 temperature of the panel to generate a bit control signal; and

a set-up control signal generator for generating a control signal such that an application time of the rising ramp waveform can be controlled in correspondence with said bit control signal and for applying the control signal to the scan driver.

- 8. The driving apparatus as claimed in claim 7, wherein said temperature sensor generates different bit control signals at a low temperature and at a temperature more than the low temperature.
- 9. The driving apparatus as claimed in claim 8, wherein

said set-up control signal generator sets a width of said control signal such that a width of the control signal applied at said low temperature is narrower than that of the control signal applied at said temperature more than the low temperature in correspondence with said bit control signal.

10. The driving apparatus as claimed in claim 9, wherein said scan driver supplies said rising ramp waveform during a time corresponding to said width of the control signal.

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- 11. The driving apparatus as claimed in claim 8, wherein said temperature sensor divides the low temperature into a plurality of temperature levels, and generates said different bit control signals for each temperature level.
- 12. The driving apparatus as claimed in claim 11, wherein said set-up control signal generator generates a control signal having a larger width as the temperature level goes lower, and said scan driver supplies said rising ramp waveform during a time corresponding to said width of the control signal.
- 13. A driving apparatus for a plasma display panel, 25 comprising:
 - a scan driver for supplying a rising ramp waveform in a set-up interval and a falling ramp waveform in a set-down interval;
- a first temperature sensor for sensing a driving 30 temperature of the panel to generate a first bit control signal;
 - a second temperature sensor for sensing a driving temperature of the panel to generate a second bit control

signal;

a set-up control signal generator for generating a first control signal such that an application time of the rising ramp waveform can be controlled in correspondence with said first bit control signal and for applying the first control signal to the scan driver; and

a set-down control signal generator for generating a second control signal such that an application time of the falling ramp waveform can be controlled in correspondence with said second bit control signal and for applying the second control signal to the scan driver.

14. The driving apparatus as claimed in claim 13, wherein said first temperature sensor generates first different bit control signals at a low temperature and at a temperature more than the low temperature, and said second temperature generates second different bit control signals at a high temperature and a temperature less than the high temperature.

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15. The driving apparatus as claimed in claim 14, wherein said set-up control signal generator sets a width of said first control signal such that a width of the first control signal applied at said low temperature is larger than that of the first control signal applied at said temperature more than the low temperature correspondence with said first bit control signal, and said set-down control signal generator sets a width of said second control signal such that a width of the second control signal applied at said high temperature narrower than that of the second control signal applied at said temperature less than the high temperature correspondence with said second bit control signal.

- 16. The driving apparatus as claimed in claim 15, wherein said scan driver supplies said rising ramp waveform during a time corresponding to said width of the first control signal, and supplies said falling ramp waveform during a time corresponding to said width of the second control signal.
- The driving apparatus as claimed in claim 14, wherein 17. said first temperature sensor divides the low temperature into a plurality of temperature levels and generates said different bit control signals for temperature level, and said second temperature sensor high temperature into a plurality divides the temperature levels and generates said second different bit control signals for each high temperature level.
 - 18. The driving apparatus as claimed in claim 17, wherein said set-up control signal generator generates a first control signal having a larger width as the low temperature level goes lower, and said scan driver supplies said rising ramp waveform corresponding to said width of the first control signal.
- 19. The driving apparatus as claimed in claim 17, wherein said set-down control signal generator generates a second control signal having a narrower width as the high temperature level goes higher, and said scan driver supplies said falling ramp waveform corresponding to said width of the second control signal.
 - 20. A method of driving a plasma display panel, comprising the steps of:

applying a rising ramp waveform to a scan electrode in a set-up interval;

applying a falling ramp waveform to the scan electrode in a set-down interval following said set-up interval; and

differently setting an application time of said falling ramp waveform applied to the scan electrode at a high temperature and at a temperature less than the high temperature.

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- 21. The method as claimed in claim 20, wherein said application time of the falling ramp waveform at said high temperature is set to be shorter than that of the falling ramp waveform at said temperature less than the high temperature.
- 22. The method as claimed in claim 21, wherein said high temperature is divided into a plurality of temperature levels, and said application time of the falling ramp waveform is more shortly set as said temperature level goes higher.
- 23. A method of driving a plasma display panel, comprising the steps of:
- applying a rising ramp waveform to a scan electrode in a set-up interval;
 - applying a falling ramp waveform to the scan electrode in a set-down interval following said set-up interval; and
- differently setting an application time of said rising ramp waveform applied to the scan electrode at a low temperature and at a temperature more than the low temperature.

- 24. The method as claimed in claim 23, wherein said application time of the rising ramp waveform at said low temperature is set to be longer than that of the rising ramp waveform at said temperature more than the low temperature.
- 25. The method as claimed in claim 24, wherein said low temperature is divided into a plurality of temperature levels, and said application time of the rising ramp waveform is longer set as said temperature level goes lower.
- 26. The method as claimed in claim 23, wherein a slope of the rising ramp waveform applied at said low temperature is equal to that of the rising ramp waveform applied at said temperature more than the low temperature.